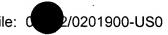
## JC03 Rec'd PCT/PTO 18 APR 2005

APR 18 2015



PCI

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Toshiyuki Nagase, et al.

Serial No.: 10/510,199

Group Art Unit: To Be Assigned

Filed: September 29, 2004

Examiner: To Be Assigned

For: CIRCUIT BOARD, METHOD OF PRODUCING SAME, AND POWER MODULE

## FILING RECEIPT STATUS REQUEST

Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

To date, we have not received a filing receipt for the above-identified patent application. If the receipt has already been sent, please forward a copy for our file. If the receipt has not been sent, please let us know when we can expect to receive it.

Respectfully submitted,

Dated: April 13, 2005

Louis J. Debuidice Reg. No. 47,522

Attorney for Applicant(s)

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